REMARKS

This Amendment is in response to the Office Action mailed 06/23/2005. In the Office Action, the Examiner objected to the drawings and rejected claims 1-31, 33, 39, 41, 43, 49, 51, and 53 under 35 U.S.C. § 102, and rejected claims 32, 34-38, 40, 42, 44-48, 50, 52, and 54-60 under 35 U.S.C. § 103. Reconsideration in light of the amendments and remarks made herein is respectfully requested.

Drawings

4.   The Examiner objects to the drawings as failing to comply with 37 CFR 1.84(p)(4) because in FIG. 7A, box 720, "OBJ(j)" should be --OBF(j)--. A corrected drawing making the requested correction is attached.

Applicant respectfully requests that the Examiner withdraw the objection to the drawings.

Rejection Under 35 U.S.C. § 102

6.   The Examiner rejects claims 1-31, 33, 39, 41, 43, 49, 51, and 53 under 35 U.S.C. § 102(e) as being anticipated by Abdallah et al. (2004/0268094).

Regarding claim 1, the Examiner asserts that Abdallah discloses the invention as claimed citing paragraph 49. Applicant respectfully disagrees. Abdallah discloses the conversion of data from one format to another wherein the data in the original format is in one architectural register and the data in the converted format is placed in another architectural register. This is entirely unlike the claimed invention which is directed to a method for translating code from a source architecture to a target architecture where an instruction format inconsistency between a source register in the source architecture and a target register of the target architecture is detected. Nothing in Abdallah discloses a conversion of instructions nor detecting an instruction format inconsistency that results from the differences between the source architecture and the target architecture. Further, nothing in Abdallah discloses consideration of the format of a register after a block of code is executed.

Regarding claim 2, the output block format (OBF) is defined in paragraph [0039] of the specification as containing "the format code of the source registers at the exit of the block." The input instruction format (IIF) is defined in paragraph [0037] of the specification as containing "the format code of the source registers." Nothing in Abdallah discloses an output block format nor an input instruction format nor a comparison between them.

Regarding claim 3, Fig. 10 of Abdallah discloses conversion of the format of a number in a first register and storing the number in the new format in a second register. This is entirely unlike converting the register format based on differences between the output block format and the input instruction format and based on the output block format asserting an access status of the source register as claimed.

Regarding claim 4, nothing in Abdallah discloses an input block format containing format of the source register expected by the source block of code before execution.

Regarding claim 5, the Examiner rejects on the same basis as for claim 3 and applicant likewise traverses on the same basis as for claim 3.

Regarding claim 6, the Examiner rejects on the same basis as for claim 3 and applicant likewise traverses on the same basis as for claim 3.

Regarding claim 7, the Examiner rejects on the same basis as for claim 3 and applicant likewise traverses on the same basis as for claim 3.

Regarding claim 8, the Examiner rejects on the same basis as for claim 3 and applicant likewise traverses on the same basis as for claim 3.

Regarding claim 9, applicant relies on the patentability of the claims from which this claim depends to traverse the rejection without prejudice to any further basis for patentability of this claim based on the additional elements recited.

Regarding claim 10, the Examiner asserts that Abdallah discloses "emitting the block inconsistency check code to be executed during an execution phase following the translation phase" citing paragraph [0012]. Paragraph [0012] merely describes the use of duplicated floating point execution resources to allow parallel execution of floating point operations. Nothing in the cited portion of Abdallah discloses executing code to check for inconsistency in register format between original code and translated code as claimed.

Regarding claim 11, the Examiner rejects on the same basis as for claim 1 and applicant likewise traverses on the same basis as for claim 1.

Regarding claim 12, the Examiner rejects on the same basis as for claims 1 and 2 and applicant likewise traverses on the same basis as for claim 1 and 2.

Regarding claim 13, the Examiner rejects on the same basis as for claim 3 and applicant likewise traverses on the same basis as for claim 3.

Regarding claim 14, the Examiner rejects on the same basis as for claim 4 and applicant likewise traverses on the same basis as for claim 4.

Regarding claim 15, the Examiner rejects on the same basis as for claim 5 and applicant likewise traverses on the same basis as for claim 5.

Regarding claim 16, the Examiner rejects on the same basis as for claim 6 and applicant likewise traverses on the same basis as for claim 6.

Regarding claim 17, the Examiner rejects on the same basis as for claim 7 and applicant likewise traverses on the same basis as for claim 7.

Regarding claim 18, the Examiner rejects on the same basis as for claim 8 and applicant likewise traverses on the same basis as for claim 8.

Regarding claim 19, the Examiner rejects on the same basis as for claim 9 and applicant likewise traverses on the same basis as for claim 9.

Regarding claim 20, the Examiner rejects on the same basis as for claim 10 and applicant likewise traverses on the same basis as for claim 10.

Regarding claim 21, the Examiner rejects on the same basis as for claim 1 and applicant likewise traverses on the same basis as for claim 1.

Regarding claim 22, the Examiner rejects on the same basis as for claims 1 and 2 and applicant likewise traverses on the same basis as for claim 1 and 2.

Regarding claim 23, the Examiner rejects on the same basis as for claim 3 and applicant likewise traverses on the same basis as for claim 3.

Regarding claim 24, the Examiner rejects on the same basis as for claim 4 and applicant likewise traverses on the same basis as for claim 4.

Regarding claim 25, the Examiner rejects on the same basis as for claim 5 and applicant likewise traverses on the same basis as for claim 5.

Regarding claim 26, the Examiner rejects on the same basis as for claim 6 and applicant likewise traverses on the same basis as for claim 6.

Regarding claim 27, the Examiner rejects on the same basis as for claim 7 and applicant likewise traverses on the same basis as for claim 7.

Regarding claim 28, the Examiner rejects on the same basis as for claim 8 and applicant likewise traverses on the same basis as for claim 8.

Regarding claim 29, the Examiner rejects on the same basis as for claim 9 and applicant likewise traverses on the same basis as for claim 9.

Regarding claim 30, the Examiner rejects on the same basis as for claim 10 and applicant likewise traverses on the same basis as for claim 10.

Regarding claim 31, the Examiner rejects on the same basis as for claim 1 and applicant likewise traverses on the same basis as for claim 1.

Regarding claim 33, nothing in Abdallah discloses a format register which is defined in paragraph [0030] of the specification as a register that keeps track of the format of the set of source registers in the source architecture and therefore cannot disclose updating of same.

Regarding claim 39, the Examiner rejects on the same basis as for claim 9 and applicant likewise traverses on the same basis as for claim 9.

Regarding claim 41, the Examiner rejects on the same basis as for claim 1 and applicant likewise traverses on the same basis as for claim 1.

Regarding claim 43, the Examiner rejects on the same basis as for claim 33 and applicant likewise traverses on the same basis as for claim 33.

Regarding claim 49, the Examiner rejects on the same basis as for claim 9 and applicant likewise traverses on the same basis as for claim 9.

Regarding claim 51, the Examiner rejects on the same basis as for claim 1 and applicant likewise traverses on the same basis as for claim 1.

Regarding claim 53, the Examiner rejects on the same basis as for claim 33 and applicant likewise traverses on the same basis as for claim 33.

Applicant respectfully requests that the Examiner withdraw the rejection of claims 1-31, 33, 39, 41, 43, 49, 51, and 53 under 35 U.S.C. § 102(e) as being anticipated by Abdallah.

Rejection Under 35 U.S.C. § 103

8.   The Examiner rejects claims 32, 34-38, 40, 42, 44-48, 50, 52, and 54-60 under 35 U.S.C. § 103(a) as being unpatentable over Abdallah et al. (2004/0268094) in view of Yates et al. (6,789,181).

Applicant respectfully points out that Abdallah is subject matter developed by another person, which qualifies as prior art only under one 35 U.S.C. § 102(e) and was, at the time the invention was made, subject to an obligation of assignment to the same person, namely Intel Corporation. Therefore, under 35 U.S.C. § 103(c), Abdallah does not preclude patentability. Applicant requests that the Examiner withdraw the rejection of claims 32, 34-38, 40, 42, 44-48, 50, 52, and 54-60 under 35 U.S.C. § 103(a) as being unpatentable over Abdallah in view of Yates.
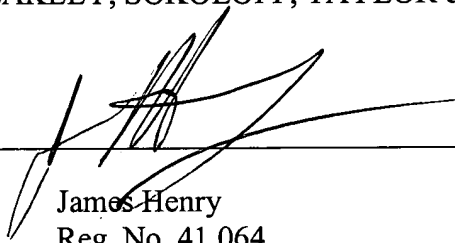
Conclusion

Applicant respectfully requests that a timely Notice of Allowance be issued in this case.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 09/21/2005   By

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Attachments: Replacement Drawing for FIG. 7A

Replacement Sheet for Figure 7A

Amendments to the Drawings:

The attached sheet of drawings includes changes to Fig. 7A. This sheet replaces the original sheet consisting of Fig. 7A. In Figure 7A, box 720, "OBJ(j)" has been changed to --OBF(j)--.

Attachment: Replacement Sheet